Comp 331 – Intro to Hardware
Finite State Machines

Moore & Mealy FSM
Implementing FSM on the BASYS-2 FPGA
Sequence Detector

Moore FSM: Present Output $z(t)$ depend only on Present State $s(t)$
see Fig 8.4 – FSM for detecting 1101 sequence

Mealy FSM: Present Output $z(t)$ depend on Present State $s(t)$ and Present Input $x(t)$.

Fig 8.5

Exercise 1: Implement on the BASYS-2 Board Example 60: Sequence Detector (either Mealy or Moore machine)

Exercise 2: Implement Example 61: Door Lock Code