Physics 313 Assignment 8

1. a) terminals of JFET = gate, drain, source

b) with no voltage difference between gate + source, JFET is on; it takes a voltage applied to the gate to reduce or turn off the current flow

c) typical input impedance for a JFET = 10^10 Ω
   this means JFET draws almost no current from what it is connected to; meaning it doesn't disturb the circuit it's connected to

d) the power supply leads for an op amp are not always shown on a circuit diagram. (if they're omitted, the assumption is that a dual supply, +Vs + -Vs, is used)

e) the two inputs of an op amp are called non-inverting (+) and inverting (-)

f) in negative feedback, the output terminal is connected to the inverting input of the op amp

g) for an ideal op amp, Rin = ∞, Rout = 0, and the open-loop gain Ao = ∞